

Notice of References Cited

Application/Control No.	Applicant(s)/Patent Under Reexamination
09/940,517	HOSHINO ET AL.

Examiner	Art Unit
Christopher R. Magee	2653

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*	B	US-4,800,454	01-1989	Schwarz et al.	360/234.5
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*	E	US-6,181,531 B1	01-2001	Koshikawa et al.	360/294.4
*	F	US-6,477,006	11-2002	Sato, Kiyoshi	360/317
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*	U	Wu et al. "ESD Protection of GMR Heads in Manufacturing." 1999. SAE Magnetics (HK) Ltd. EOS/ESD SYMPOSIUM. pages 99-315 to 99-318.
*	V	IBM Technical Disclosure Bulletin. "Magnetoresistive Head with Integrated Varistor for ESD/EOS Protection." April 1, 2000. IBM. Issue Number 432. Page Number 751.
*	W	IBM Technical Disclosure Bulletin. "Magnetoresistive Head with Integrated Varistor Employing Combined Varistor, Antiferromagnetic Seed/Gap and Grounded First Shield." April 1, 2000. IBM. Issue Number 432. Page Number 751.
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
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